

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>( Not for submission under 37 CFR 1.99)</i>	Application Number		10715459
	Filing Date		2003-11-19
	First Named Inventor		Curtis Davis
	Art Unit		2183
	Examiner Name		Keith E. Vicary
	Attorney Docket Number		NVDA/AG-08-0112-US

**U.S.PATENTS**

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1	SUZUZU et al. "A Microprocessor with a 128-Bit CPU, Ten Floating MAC's, Four Floating-Point Dividers, and an MPEG-2 Decoder," IEEE Journal of Solid State Circuits, Vol. 34, November 1999, pp. 1608-1618.	<input type="checkbox"/>
2	Office Action, Ser. No. 10/715,400, dated February 16, 2010.	<input type="checkbox"/>

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<sup>1</sup> See Kind Codes of USPTO Patent Documents at [www.USPTO.GOV](http://www.USPTO.GOV) or MPEP 901.04. <sup>2</sup> Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>3</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>4</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>5</sup> Applicant is to place a check mark here if English language translation is attached.